

Notice of References Cited	Application/Control No. 09/822,512		Applicant(s)/Patent Under Reexamination HACHIYA, KOUTARO	
	Examiner Fred Ferris		Art Unit 2128	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,601,080	07-2003	Garg, Rajat P.	708/502
	B	US-6,144,932	11-2000	Hachiya, Koutarou	703/14
	C	US-6,636,828	10-2003	Hachiya, Koutaro	703/2
	D	US-5,655,137	08-1997	Kevorkian, Aram K.	712/17
	E	US-6,031,979	02-2000	Hachiya, Koutaro	716/7
	F	US-6,470,368	10-2002	Garg et al.	708/446
	G	US-6,397,236	05-2002	Garg et al.	708/446
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	EP 1 143 346 A2	03-2001	JP	Hachiya	G06F 17/12
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"A New Matrix Solution Technique for General Circuit Simualtin", R. Burch, IEEE Transactions on Computer Aided Design of IC's, vol. 12, No. 2, February 1993
	V	"Parallel Programming with Control Abstraction", L.A. Crowl, ACM transactions on Prog. Lang., Vol. 16, No. 3, May 1994
	W	"On Efficient Band Matrix Arithmetic", W. Eberly, IEEE 0-8186-2900-2/92, IEEE 1992
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.